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SmartFusion2 SoC FPGA Architecture SmartFusion2 SoC FPGAs offer 5K-150K LEs with a 166MHz ARM® Cortex-M3 processor, including ETM and Instruction Cache with on-chip eSRAM & eNVM and a complete Microcontroller Subsystem with extensive peripherals including CAN, TSE, USB. Architecture highlights include:

SmartFusion2 SoC FPGAs | Microsemi

Oversampling Disadvantages www.ti.com As shown in Figure 3, 70-MHz IF in the third Nyquist Zone is aliased back in the first Nyquist Zone centered at 14 MHz with a 56-MSPS undersampling rate.

Why Use Oversampling when Undersampling Can do the Job

A Multi-Gigabit Transceiver (MGT) is a SerDes capable of operating at serial bit rates above 1 Gigabit/second. MGTs are used increasingly for data communications because they can run over longer distances, use fewer wires, and thus have lower costs than parallel interfaces with equivalent data throughput.

Multi-gigabit transceiver - Wikipedia

The Embedded Design Handbook complements the primary documentation for the Intel tools for embedded system development. It describes how to most effectively use the tools, and recommends design styles and practices for developing, debugging, and optimizing embedded systems using Intel-provided tools.

Embedded Design Handbook - intel.com

iCE40 FPGA devices maximize battery life and minimize power consumption for ultra-low power, always-on applications. Up to 7680 programmable logic cells.

iCE40 from Lattice - iCE40 LP/HX/LM - Lattice Semiconductor

MIL-STD-188 is a series of U.S. military standards relating to telecommunications

MIL-STD-188 - Wikipedia

MachXO3 FPGA family provides instant-on, non-volatile, small- footprint FPGAs for bridging applications such as MIPI DSI/CSI-2 interfaces.

MachXO3 - Lattice Semiconductor

Microsemi Corporation, a wholly owned subsidiary of Microchip Technology Inc. (Nasdaq: MCHP), offers a comprehensive portfolio of semiconductor and system solutions for communications, defense & security, aerospace and industrial markets.

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Other variants (with or without pin headers, other speed grades, extended temperature range) can be produced on request. The LX75 FPGA Board (1.15b) is the largest variant which is supported by the free ISE Webpack version.

USB-FPGA Module 1.15: Spartan 6 LX45, LX75 and LX150 FPGA

free PCB design tools, free electronic circuit designs, and electronics resources

Free Electronics resources: schematics, tutorials

Stratix 10 GX FPGA L-Tile DK-DEV-1SGX-L-A Stratix 10 GX FPGA H-Tile DK-DEV-1SGX-H-A Board and FPGA capabilities vary depending on the development kit version selected. For more information on the Stratix 10 L-tile and H-tile, refer to Stratix 10 FPGA product page on Intel's website. board_design

Intel Stratix 10 GX FPGA Development Kit User Guide

Under review as a conference paper at ICLR 2017 SQUEEZENET: ALEXNET-LEVEL ACCURACY WITH 50X FEWER PARAMETERS AND <0.5MB MODEL SIZE Forrest N. Iandola 1, Song Han2, Matthew W. Moskewicz , Khalid Ashraf1,

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HIGH PERFORMANCE SOFTWARE DEFINED RECEIVER 10 kHz â€“ 3.0 GHz Super Wideband P25, NXDN â„¸, dPMR â„¸, D -STAR Mode Real-time Spectrum Scope with Waterfall

HIGH PERFORMANCE SOFTWARE DEFINED RECEIVER

USER GUIDE AND SPECIFICATIONS NI myRIO-1900 The National Instruments myRIO-1900 is a portable reconfigurable I/O (RIO) device that students can use to design control, robotics, and mechatronics systems.

myRIO-1900 User Guide and Specifications - National

Platform Cable USB II DS593 (v1.5.1) August 6, 2018 www.xilinx.com 3 Physical Description The Platform Cable USB II electronics are housed in a recyclable, fire-retardant plastic case (Figure 2).

Platform Cable USB II - Xilinx

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NI 9853 Operating Instructions 4 ni.com Caution For Zone 2 applications, install a protection device between the CAN signals and the NI 9853 CAN

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